


ISSUE CLASSIFICATION	
Class	Subclass

## U.S. UTILITY Patent Application

U.S. OFFICE Patent Application	
O.I.P.E.  SCANNED <i>HKm 4</i> Q.A. <i>ALL 4</i>	PATENT DATE

APPLICATION NO. 09/941360	CONT/PRIOR D	CLASS 205	SUBCLASS 150	ART UNIT 1741	EXAMINER 42
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## APPLICANTS

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**title**

Method and apparatus for plating and polishing a semiconductor substrate

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
Amount Due			Date Paid	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____			<b>ISSUE BATCH NUMBER</b>	
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